MILITARY SPECIFICATION

MICROCIRCUITS, DIGITAL, CMOS 4096 BIT STATIC
RANDOM ACCESS MEMORY (RAM)
MONOLITHIC SILICON

This amendment forms a part of MIL-M-38510/289, dated 4 December 1986,
and is approved for use by all Departments and Agencies of the
Department of Defense.

PAGE 1

* 1.2.3, add the following new case outline:

Letter	Case outline (see MIL-M-38570, appendix C)
Y	F-10 (18 lead, .540 x .370 x .092) flat package.

PAGE 8

4.4.1c, line 4: Delete "of 50 mV rms", substitute "not to exceed 50 mV rms".

PAGE 12

* FIGURE 2, terminal connection, device types 01, 02, 03, and 04: Delete case X
  (two places) and substitute cases X and Y (two places).

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FIGURE 5, note 2: Delete "200Ω ±5%", substitute "2000Ω minimum".

PAGES 21 through 23

* TABLE III, cases column heading: Delete cases 3, V, X and substitute cases
  3, V, X, and Y.

PAGES 25 through 27

* TABLE III, cases column heading: Delete cases 3, V, X and substitute cases
  3, V, X, and Y.

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TABLE III, C_i, test no. 52, pin 18 (V_{CC}): Delete "6/", substitute "GND".

TABLE III, C_o, test no. 53, pin 18 (V_{CC}): Add "GND".

The margins of this amendment are marked with an asterisk to indicate where changes (additions, modifications, corrections, deletions) from the previous amendment were made. This was done as a convenience only and the Government assumes no liability whatsoever for any inaccuracies in these notations. Bidders and contractors are cautioned to evaluate the requirements of this document based on the entire content irrespective of the marginal notations and relationship to the last previous amendment.

CONCLUDING MATERIAL

Custodians:
  Army - ER
  Navy - EC
  Air Force - 17

Review activities:
  Army - AR, MI
  Navy - OS, SH, TD
  Air Force - 19, 85, 99
  DLA - ES

User activities:
  Army - SM
  Navy - AS, CG, MC

Preparing activity:
  Air Force - 17

Agent:
  DLA - ES

(Project 5962-1198)